

**ECE544: Fault-Tolerant Computing & Reliability Engineering  
(Fall 2022)**

**Homework #2 Solution  
(45 points)**

1. How many check bits are needed if the Hamming correcting code is used to detect single bit errors in a 64-bit data word? (5 points)

**Solution:**

Need K check bits such that:  $64 + K \leq 2^K - 1$ .

The minimum value of K, which satisfies this condition, is 7.

2. Develop an SEC code for a 16-bit data word. 1) Generate the code for the data word *0101000000111001*. 2) Show that the code will correctly identify an error in data bit D<sub>16</sub>. (40 points)

**Solution:**

- 1) (30 points)

Step 1 (4 points): According to the inequality  $2^k - 1 \geq M + K$ , where M=16, 5 check bits are needed for an SEC code for 16-bit data words.

Step 2 (9 points): The layout of data bits and check bits:

Bit Position	Position Number	Check Bits	Data Bits
21	10101		D <sub>16</sub>
20	10100		D <sub>15</sub>
19	10011		D <sub>14</sub>
18	10010		D <sub>13</sub>
17	10001		D <sub>12</sub>
16	10000	C16	
15	01111		D <sub>11</sub>
14	01110		D <sub>10</sub>
13	01101		D <sub>9</sub>
12	01100		D <sub>8</sub>
11	01011		D <sub>7</sub>
10	01010		D <sub>6</sub>
9	01001		D <sub>5</sub>
8	01000	C8	
7	00111		D <sub>4</sub>
6	00110		D <sub>3</sub>

5	00101		D <sub>2</sub>
4	00100	C <sub>4</sub>	
3	00011		D <sub>1</sub>
2	00010	C <sub>2</sub>	
1	00001	C <sub>1</sub>	

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Step 3 (15 points): The check bits are calculated

$$C_1 = D_1 \oplus D_2 \oplus D_4 \oplus D_5 \oplus D_7 \oplus D_9 \oplus D_{11} \oplus D_{12} \oplus D_{14} \oplus D_{16}$$

$$C_2 = D_1 \oplus D_3 \oplus D_4 \oplus D_6 \oplus D_7 \oplus D_{10} \oplus D_{11} \oplus D_{13} \oplus D_{14}$$

$$C_4 = D_2 \oplus D_3 \oplus D_4 \oplus D_8 \oplus D_9 \oplus D_{10} \oplus D_{11} \oplus D_{15} \oplus D_{16}$$

$$C_8 = D_5 \oplus D_6 \oplus D_7 \oplus D_8 \oplus D_9 \oplus D_{10} \oplus D_{11}$$

$$C_{16} = D_{12} \oplus D_{13} \oplus D_{14} \oplus D_{15} \oplus D_{16}$$

For the word  $D_{16}D_{15}D_{14}\dots D_2D_1 = \mathbf{0101000000111001}$ , the check bits are

$$C_{16} = 0; C_8 = 0; C_4 = 0; C_2 = 0; C_1 = 1.$$

Step 4 (2 points): The code word is **0101000000110100101**

**2) (10 points)**

If an error occurs in data bit  $D_{16}$ , the check bits become

$$C_{16} = 1; C_8 = 0; C_4 = 1; C_2 = 0; C_1 = 0.$$

Comparing the two sets of check bits forms the syndrome word:

	C <sub>16</sub>	C <sub>8</sub>	C <sub>4</sub>	C <sub>2</sub>	C <sub>1</sub>
	0	0	0	0	1
⊕	1	0	1	0	0
	1	0	1	0	1

The result indicates an error identified in bit position 21, which is data bit  $D_{16}$