ECE454/544: Fault-Tolerant Computing & Reliability Engineering



Lecture #12– Binary Decision Diagrams (BDD)

Instructor: Dr. Liudong Xing Fall 2022

Administrative Issues (10/24, Mon.)

- Project meeting (in-person or virtual)
 - Due by Oct. 28, Friday
- Homework#5 assigned today
 - Due by Oct. 31, Monday
- Today's topics
 - Finish Lecture#11 (RBD)
 - Then Lecture#12 (BDD)

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- A RBD is a **success-oriented** network describing the function of the system
- In terms of modeling capability, RBD is equivalent to the static/traditional fault trees, and they can be converted into each other easily
- Path-sets and cut-sets can be generated from both RBD and fault trees
- I/E and SDP can be applied to the quantitative analysis based on both path sets and cut sets

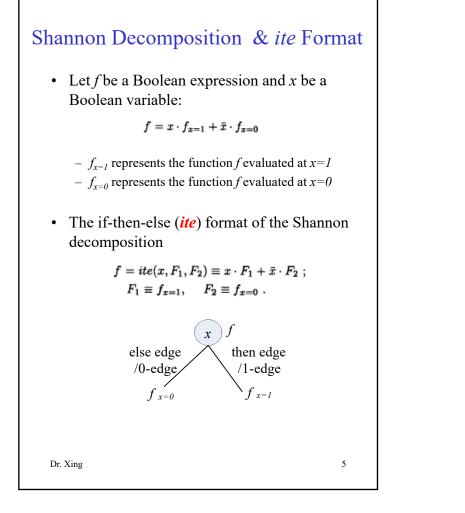
Topics

- Binary decision diagrams (BDD)
 - Basic concepts
 - BDD manipulation & construction
 - Variable ordering in BDD
 - Calculating (un)-reliability from the BDD

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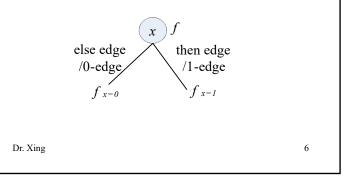
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Binary Decision Diagrams (BDD)

- A BDD is a directed acyclic graph (DAG) based on Shannon decomposition
- A BDD is a binary tree
 - two sink nodes labeled with constants 1 and 0
 - each non-sink node is labeled with a Boolean variable x and has two outgoing edges called 1-edge (then-edge) and 0edge (else-edge), respectively

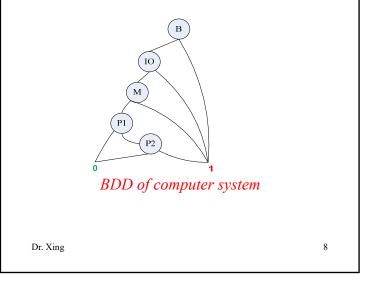




- An ordered BDD (OBDD) is a BDD with the constraint that the variables are ordered and every source-to-sink path in the OBDD visits the variables in an ascending order
- A reduced OBDD (ROBDD) is an OBDD where each node represents a distinct Boolean expression

ROBDD

- ROBDD are widely used in practice!
- In reliability engineering:
 - Sink node 1: the system fails
 - Sink node 0: the system is functioning
 - -x=1: component x is failed
 - x=0: component x is functioning





- Binary decision diagrams (BDD)
 - $\sqrt{\text{Basic concepts}}$
 - **BDD manipulation & construction**
 - Variable ordering in BDD
 - Calculating (un)-reliability from the BDD

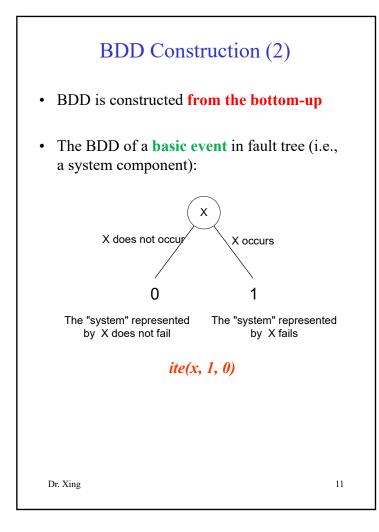
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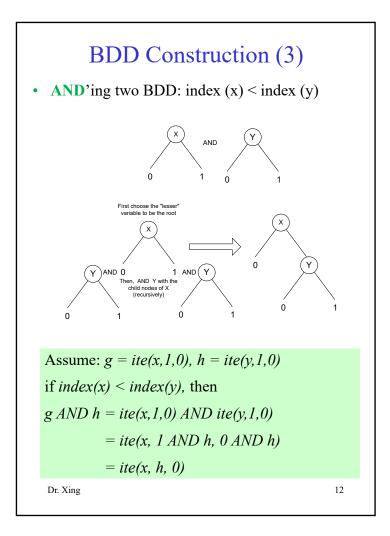
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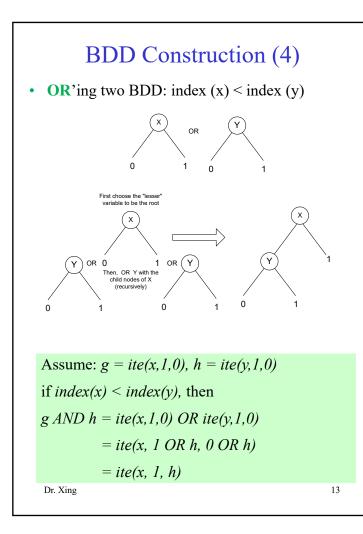
BDD Construction (1)

- To generate ROBDD, the ordering of the variables (basic events) must be selected
 - An index is assigned to each variable to indicate its position in the ordering
 - The order is not changed during the generation
 - Index(x) < index (y) implies that y is behind x in the order of variables

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Combination Operation on BDD

• Let Boolean expression *g* and *h* be:

 $g = ite(x, g_{x=1}, g_{x=0}) = ite(x, G_1, G_2)$ $h = ite(y, h_{y=1}, h_{y=0}) = ite(y, H_1, H_2)$

 A logic operation (AND/OR, ^{\$}) between g and h can be represented by the following BDD manipulation:

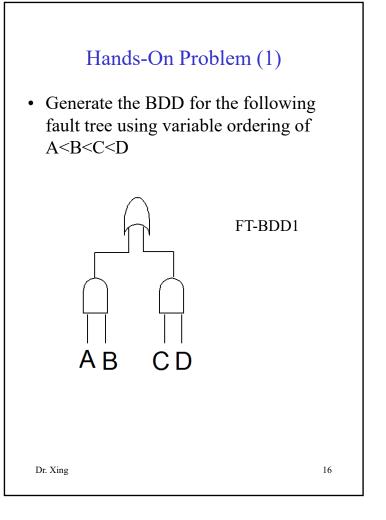
$$\begin{split} ite(x,G_1,G_2) \diamond ite(y,H_1,H_2) = \\ \begin{cases} ite(x,G_1 \diamond H_1,G_2 \diamond H_2) & index(x) = index(y) \\ ite(x,G_1 \diamond h,G_2 \diamond h) & index(x) < index(y) \\ ite(y,g \diamond H_1,g \diamond H_2) & index(x) > index(y) \end{cases} \end{split}$$

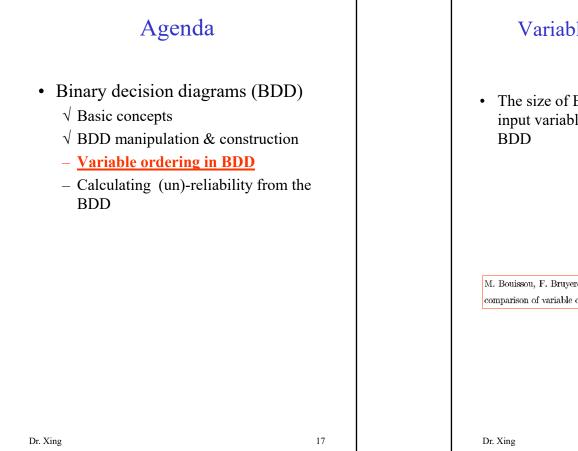
• The same rule can be used for logic operation between sub-expressions **until one of them becomes a constant 0 or 1**

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- To achieve ROBDD, two reductions are performed as the BDD is built. These ensure that the BDD that results is minimal for the chosen ordering
 - Merge isomorphic subtrees (if two identical sub-BDD result, at least one is superfluous)
 - <u>Delete useless nodes</u> (A node with two equal children is useless and should be replaced with one of its children.)

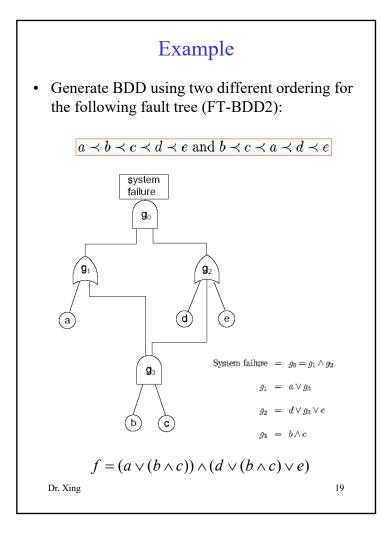


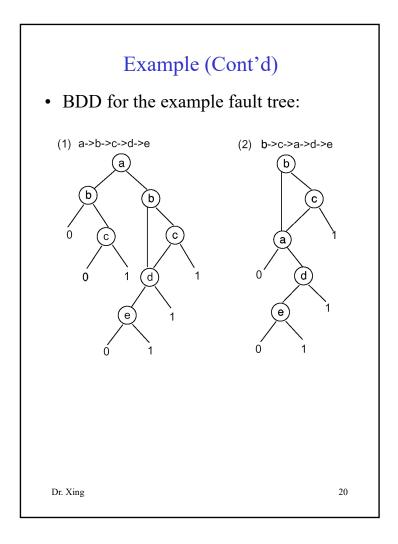


Variable Ordering in BDD

• The size of BDD depends heavily on the input variable ordering used to build the BDD

M. Bouissou, F. Bruyere, and A. Rauzy, "BDD based fault-tree processing: A comparison of variable ordering heuristics," *ESREL'97 conference*, June 1997.

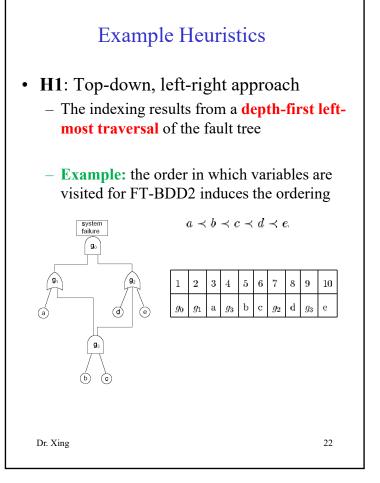


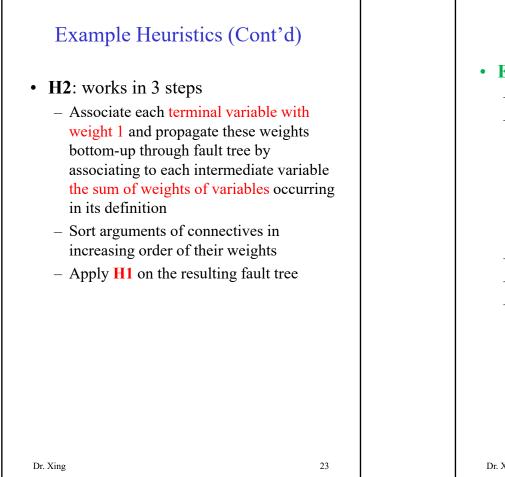


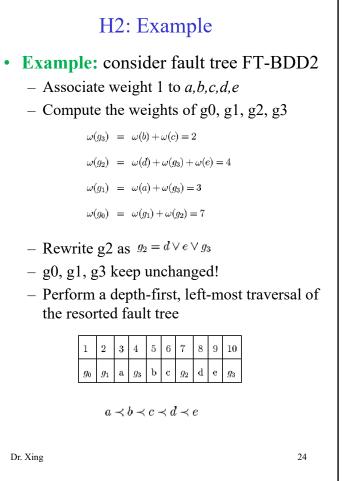
Note!

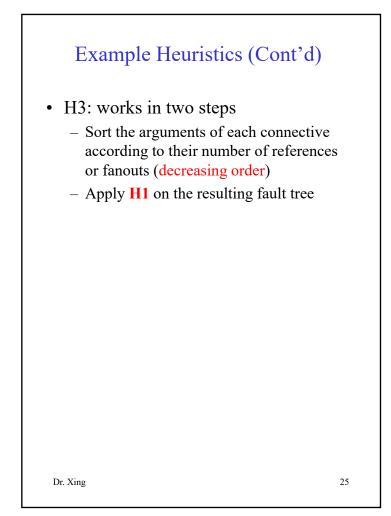
- The poor ordering can significantly affect the size of the BDD, thus the reliability analysis solution time for large systems!
- Currently there is no rule-based means of determining the best way of ordering basic events for a given fault tree structure
- Fortunately heuristics can usually be used to find a reasonable variable ordering









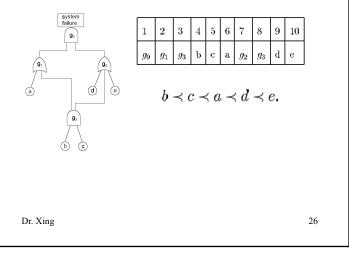


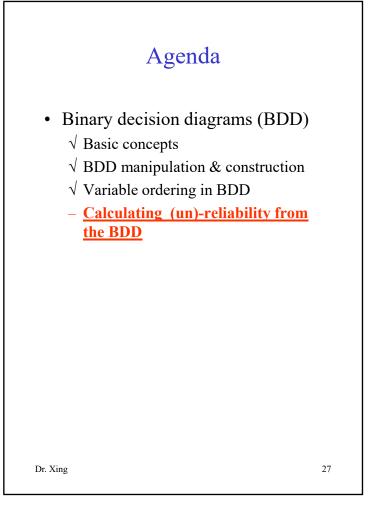
H3: Example

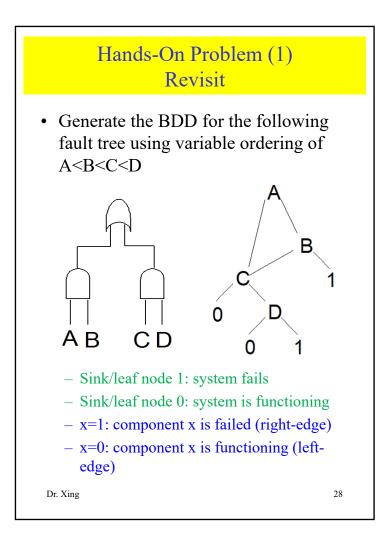
- **Example:** consider FT-BDD2
 - g1, g2 have only 1 fanout → definition of g0 remain unchanged
 - g3 has 2 fanouts (g1, g2), a,d,and e have 1 fanout → g1, g2 are rewritten as

$$g_1 = g_3 \lor a \qquad \qquad g_2 = g_3 \lor d \lor e_3$$

- b, c have 1 fanout → definition of g3 remains unchanged!
- Visiting order resulted from a depth-first, left-most traversal of rewritten fault tree

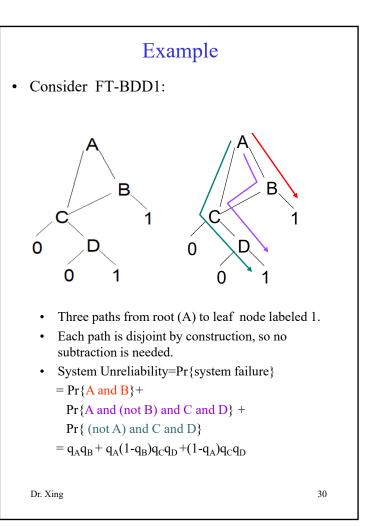


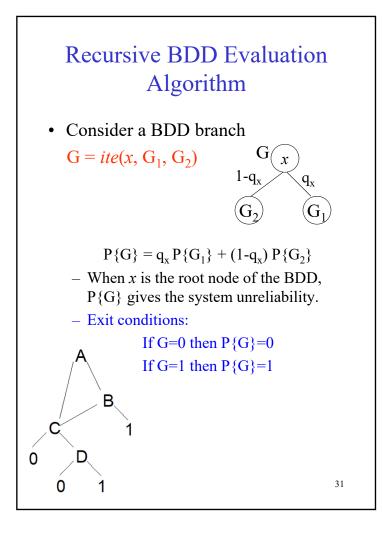


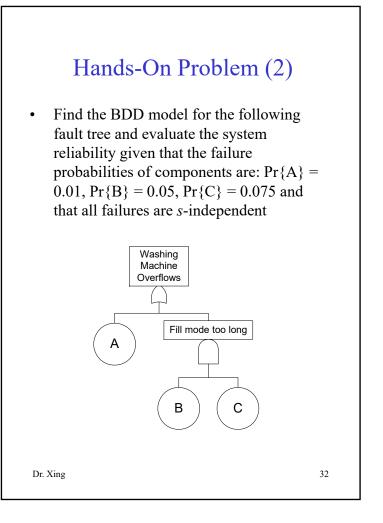


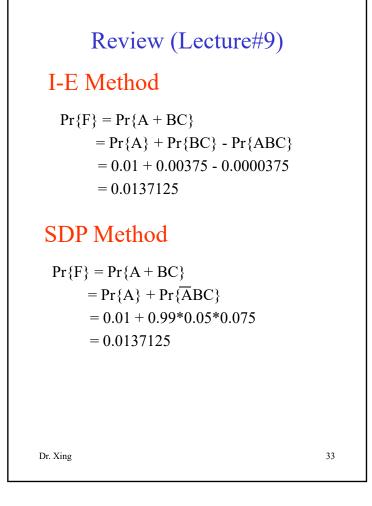
Calculating Unreliability using BDD

- Each path through the BDD from the root to a leaf node represents a disjoint combination of component failures and non-failures
- A path with a leaf node labeled with a 1 leads to system failure
- Probabilities associated with arcs on each path are either q (component failure probability) for the right branch or (1-q)(component reliability) for the left branch
- System **unreliability** is given by the sum of the probabilities for all paths from the root to a leaf node labeled 1
- System **reliability** is given by the sum of the probabilities for all paths from the root to a leaf node labeled **0**



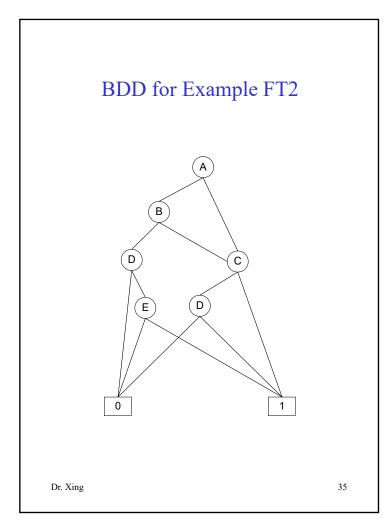


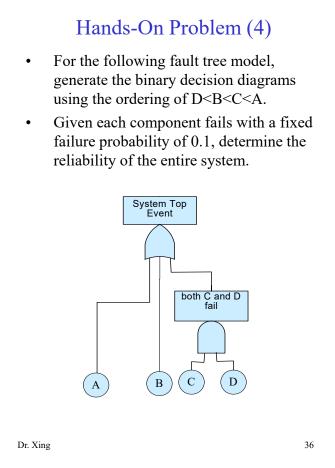




Hands-On Problem (3) For the fault tree called example FT2 in L#9, generate the BDD and calculate the probability of occurrence for the top event in the fault tree. Assume that the probability of occurrence for each of the basic events is: Pr{A} = 0.05, Pr{B} = 0.10, Pr{C} = 0.15, Pr{D} = 0.20, Pr{E} = 0.25

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References	
• J. B. Dugan and S. A. Doyle. "New Results in Fault-Tree Analysis" Tutorial Notes of the Annual Reliability and Maintainability Symposium, January 1997	
• R. E. Bryant, "Graph-based algorithms for boolean function manipulation," IEEE Trans. of Computers, vol. C-35, no. 8, pp. 677–691, August 1986.	on
• A. Rauzy, "New algorithms for fault tree analysis," Reliability Engineering and System Safety, vol. 40, pp. 203–211, 1993.	
• M. Bouissou, F. Bruyere, and A. Rauzy. "BDE Based Fault-Tree Processing: A Comparison o Variable Ordering Heuristics", Proceedings of ESREL'97 conference, June 1997.	f
 L. Xing and S. V. Amari, Binary Decision Diagrams and Extensions for System Reliability Analysis, Wiley-Scrivener, MA, ISBN: 978-1- 118-54937-7, July 2015 	
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Summary of Lecture #12

- BDD can be used to efficiently and accurately solve the combinatorial reliability models without the use of cutsets
- The size of the BDD depends heavily on the input variable ordering

Next topic:

Sensitivity Analysis

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